Edward Grochoswski et al.

EAST SEARCH

9/2/05

L#	Hits	Search String	Databases
S1	37055	(((integrated or digital) near2 circuit$1) or processor$1) with (power near2 (distribution or netwc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S2	1486	S1 and (power near2 network$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S3	66	S2 and (power with (voltage near2 range))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S4	334	S2 and (network$1 with response)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S5	7	S2 and (network$1 with (response near2 function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S6	303	S1 and ((power near2 network$1) with device$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S7	169	S6 and (device$1 with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S8	54	S6 and (device$1 with current with (amplitude$1 or value$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S9	2	S2 and ("shift register" with current with (amplitude$1 or value$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S11	35	S1 and (network$1 with (response near2 function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S12	2	S2 and (network$1 with response with adder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S13	2	S2 and (power with response with adder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S14	6	S2 and (response with adder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S15	6	S2 and (weight$1 with adder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S17	45	S2 and ((compar$4 near2 voltage) with threshold)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S18	24	S2 and ((limit$3 or adjust$3 or throttl$3) near2 (operation$1 or unit$1) with voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S19	6	S2 and ((limit$3 or adjust$3 or throttl$3) near2 (operation$1 or unit$1) with (threshold or range	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S20	3	S2 and (pipeline with fetch$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S21	24	S2 and (clock near2 gating)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S22	3	S2 and ((clock near2 gating) with instruction$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S23	13	S2 and (inputs with adder)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S24	2	S2 and (recursion near2 coefficient$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S25	2	S2 and ((estimat$3 near2 voltage$1) with sequence)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S26	3	S2 and (current near2 coefficient$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S27	8	S2 and (voltage near2 coefficient$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S28	2	S1 and (recursion near2 coefficient$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S29	7	S1 and ((clock near2 gating) with instruction$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S30	44	S1 and (pipeline with fetch$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S31	99	S1 and (current near2 coefficient$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S32	159	S2 and (processor with (execut$3 near2 instruction$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S33	4	S2 and ((estimat$3 near2 voltage$1) with processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S34	5	S2 and ((compar$4 near2 voltage) with range)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S35	35	S2 and (processor with pipeline$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S36	6	S2 and (processor with pipeline$1 with (double or multiple))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S37	14	S2 and ((gating or "gate unit") with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S38	3	S2 and ((gating or "gate unit") with pipeline)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

S39	2	S2 and (entry with "shift register")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S40	16	S1 and (entry with "shift register")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S41	2	S2 and (impulse with (response near2 function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S42	12	S2 and (impulse with function)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S43	136	S2 and (response with function)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S44	301	S3 or S5 or S8 or S9 or S10 or S11 or S12 or S13 or S14 or S15 or S16 or S17 or S18 or S1...	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S45	776	S4 or S6 or S31 or S32 or S43	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S46	147	S44 and S45	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S47	301	S44 or S46	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S16	5	S2 and ((estimat$3 near2 voltage) with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S48	3917	((integrated or digital) near2 circuit$1) and (power near2 network)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S49	11	S48 and ((estimat$3 near2 voltage) with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S50	212	S48 and (voltage with (variation or varies) with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S51	8	S48 and (voltage with (variation or varies) with (comput$3 or estimat$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S52	7	S48 and (voltage with (variation or varies) with model$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S53	7	S48 and (current with (variation or varies) with model$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S54	7	S2 and ("shift register" with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S10	18	S1 and ("shift register" with current with (amplitude$1 or value$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S55	12	S2 and (current with (clock adj cycle$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S56	29	S2 and (response near2 function)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S57	7	S56 and (network with (response near2 function))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S58	37068	(((integrated or digital) near2 circuit$1) or processor$1) with (power near2 (distribution or netw...	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S59	1486	S58 and (power near2 network$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S60	2	S59 and (current with (successive adj cycle$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S61	2.	S59 and (current with (successive adj interval$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S62	12	S59 and (current with (clock adj cycle$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S63	4	5,948,106.pn. or "6,304,978".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S64	164	703/18.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S65	54	S64 and (power near2 (network$1 or circuitry))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S66	40	S65 and (voltage with current)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S67	25	**S66 and (voltage with (threshold or limit))**	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S68	3920	((integrated or digital) near2 circuit$1) and (power near2 network)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S69	0	S68 and ("sucessive clocks".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S70	0	S68 and ("sucessive cycles".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S71	0	S68 and ("sucessive intervals".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S72	21	S68 and ("clock cycles".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S73	13	S68 and ("current consumption".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S74	10	S68 and ("current amplitudes".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S75	71	S68 and ("current values".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S76	108	S72 or S73 or S74 or S75	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S77	29	S76 and (clocks.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S78	34	S76 and (cycles.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S79	21	S76 and (intervals.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

Results of search set S47

Document Kind Codes Title	Issue Date	Current OR	Abstract
US 20050182584 A1 Method and system for identifying and locating defects in an integrated circuit	20050818	702/117	
US 20050177150 A1 Electrosurgical generator	20050811	606/34	
US 20050174239 A1 Radio frequency identification tag antenna configurations	20050811	340/572.1	
US 20050169341 A1 Reduced-maintenance excimer laser with oil-free solid state pulser	20050804	372/81	
US 20050152354 A1 Method and systems for ordered dynamic distribution of packet flows over network processing	20050714	370/389	
US 20050149292 A1 Method for detecting communication impulses of a fan motor and circuit arrangement for carryi	20050707	702/183	
US 20050140209 A1 Power distribution web node and power management process	20050630	307/9.1	
US 20050136912 A1 Security and tracking system	20050623	455/423	
US 20050132241 A1 System and method for providing remote monitoring of voltage power transmission and distribu	20050616	713/300	
US 20050114468 A1 Method for configuring a piece of equipment with the use of an associated machine resolvable	20050526	709/217	
US 20050114033 A1 Method and system for transmission of seismic data	20050526	702/14	
US 20050110551 A1 Damping of LC Ringing in IC (Integrated Circuit) Power Distribution Systems	20050526	327/310	
US 20050108582 A1 System, architecture, and method for logical server and other network devices in a dynamicall,	20050519	713/300	
US 20050090243 A1 System and method for emulating a telephony driver	20050428	455/423	
US 20050086366 A1 Home system including a portable fob having a display	20050421	709/238	
US 20050085248 A1 Home system including a portable fob mating with system components	20050421	455/500	
US 20050085180 A1 Home system including a portable fob having a rotary menu and a display	20050421	455/1	
US 20050081073 A1 Method and apparatus for controlling the power consumption of a semiconductor device	20050414	713/320	
US 20050071106 A1 Expandable intelligent electronic device	20050331	702/104	
US 20050060527 A1 Automatic configuration of equipment software	20050317	713/1	
US 20050040974 A1 Method, system, and apparatus for remote data calibration of a RFID tag population	20050224	341/53	
US 20050027464 A1 Revenue meter with power quality features	20050203	702/61	
US 20050021874 A1 Single chip protocol converter	20050127	709/250	
US 20050001601 A1 Circuit for controlling the minimum operating voltage of switching power supply	20050106	323/282	
US 20040267513 A1 Automatic creation of a design for a high or medium voltage power transmission network	20041230	703/18	
US 20040254750 A1 REVENUE METER BAYONET ASSEMBLY AND METHOD OF ATTACHMENT	20041216	702/61	
US 20040249588 A1 Operation analyzing method for semiconductor integrated circuit device, analyzing system use(	20041209	702/66	
US 20040247961 A1 Auxiliary fuel cell system	20041209	429/22	
US 20040243958 A1 Voltage island chip implementation	20041202	716/7	
US 20040238850 A1 Integrated circuit device and method for forming the same	20041202	257/202	
US 20040236972 A1 Firmware controlled dynamic voltage adjustment	20041125	713/300	
US 20040233142 A1 Display device	20041125	345/76	
US 20040225388 A1 Fully digitized audio system	20041111	700/94	

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US 20040221185 A1	Processor with low overhead predictive supply voltage gating for leakage power reduction	20041104	713/300
US 20040215750 A1	Configuration program for a security system	20041028	709/220
US 20040212503 A1	Communications architecture for a security network	20041028	340/572.1
US 20040212500 A1	RFID based security network	20041028	340/541
US 20040212497 A1	Multi-controller security network	20041028	340/539.17
US 20040212494 A1	Cordless telephone system	20041028	340/539.1
US 20040212493 A1	RFID reader for a security network	20041028	340/531
US 20040207527 A1	Identification tag utilizing charge pumps for voltage supply generation and data recovery	20041021	340/572.4
US 20040196731 A1	Semiconductor integrated ciruit device	20041007	365/233
US 20040196470 A1	Raster image processing (RIP) manager for enhanced print shop operation	20041007	358/1.1
US 20040189271 A1	Power supply system and apparatus	20040930	323/283
US 20040162642 A1	Thin client power management system and method	20040819	700/286
US 20040158663 A1	Interconnect topology for a scalable distributed computer system	20040812	710/100
US 20040151257 A1	Predistortion calibration in a transceiver assembly	20040805	375/296
US 20040141759 A1	System and method for dynamic bandwidth allocation in PONs	20040722	398/168
US 20040136712 A1	Integrated PON processor	20040715	398/60
US 20040136534 A1	System and method for improved data protection in PONs	20040715	380/256
US 20040128625 A1	Resonance reduction arrangements	20040701	716/1
US 20040124715 A1	Apparatus and method for resonance reduction	20040701	307/127
US 20040124713 A1	Distributed power supply arrangement	20040701	307/69
US 20040095193 A1	Radio frequency power amplifier adaptive bias control circuit	20040520	330/285
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US 20040085121 A1	System and method for reducing a leakage current associated with an integrated circuit	20040506	327/544
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US 20030202655 A1	Managing power in a line powered network element	20031030	379/413
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US 20030135351 A1	Functional pipelines	20030717	702/186
US 20030125923 A1	Simulation of di/dt-induced power supply voltage variation	20030703	703/20
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US 20030067725 A1	Distributed monitoring and protection system for a distributed power network	20030410	361/65
US 20030065459 A1	Expandable intelligent electronic device	20030403	702/62
US 20030046396 A1	Systems and methods for managing resource utilization in information management environme	20030306	709/226
US 20030043038 A1	Modulating current of an integrated circuit for signal transmission	20030306	340/568.2

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US 20030034825 A1	System and method for disconnecting a portion of an integrated circuit	20030220	327/530
US 20030025593 A1	GEOMETRIC HARMONIC MODULATION SIGNALING AND DETECTION	20030206	340/310.01
US 20030021261 A1	Internet telephony gateway system	20030130	370/352
US 20030014200 A1	Revenue meter with power quality features	20030116	702/60
US 20020199160 A1	Power supply network analyzing method, computer program for executing the method, storage	20021226	716/5
US 20020194251 A1	Systems and methods for resource usage accounting in information management environment	20021219	718/105
US 20020190797 A1	Standby circuit for an electrical device	20021219	330/297
US 20020174227 A1	Systems and methods for prioritization in information management environments	20021121	709/226
US 20020171407 A1	Method and apparatus for controlling current demand in an integrated circuit	20021121	323/364
US 20020167405 A1	Radio frequency identification architecture	20021114	340/572.1
US 20020165677 A1	Apparatus and method for seamlessly upgrading the firmware of an intelligent electronic device	20021107	702/62
US 20020162014 A1	Intelligent electronic device with assured data storage on powerdown	20021031	713/200
US 20020158689 A1	CONTROLLER-BASED RADIO FREQUENCY AMPLIFIER MODULE AND METHOD	20021031	330/129
US 20020152305 A1	Systems and methods for resource utilization analysis in information management environmen	20021017	709/224
US 20020152044 A1	Method, system, and apparatus for remote timing calibration of a RFID tag population	20021017	702/106
US 20020149483 A1	Method, System, and apparatus for communicating with a RFID tag population	20021017	340/572.1
US 20020149482 A1	Identification tag utilizing charge pumps for voltage supply generation and data recovery	20021017	340/572.1
US 20020149481 A1	Method, system, and apparatus for binary traversal of a tag population	20021017	340/572.1
US 20020149480 A1	Method, system, and apparatus for remote data calibration of a RFID tag population	20021017	340/572.1
US 20020149416 A1	Efficient charge pump apparatus	20021017	327/536
US 20020133593 A1	Systems and methods for the deterministic management of information	20020919	709/226
US 20020120741 A1	Systems and methods for using distributed interconnects in information management environme	20020829	709/225
US 20020112212 A1	Method for supply voltage drop analysis during placement phase of chip design	20020815	716/1
US 20020106090 A1	Reverberation processor based on absorbent all-pass filters	20020808	381/63
US 20020105231 A1	Voltage recovery device for use with a utility power network	20020808	307/64
US 20020104031 A1	Programmable power management system and method	20020801	713/320
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US 20020007464 A1	Apparatus and method for modular dynamically power managed power supply and cooling sys	20020117	713/320
US 20020004915 A1	System, method, architecture, and computer program product for dynamic power managemer	20020110	713/320
US 20020004913 A1	Apparatus, architecture, and method for integrated modular server system providing dynamica	20020110	713/300
US 20020004912 A1	System, architecture, and method for logical server and other network devices in a dynamically,	20020110	713/300
US 6934951 B2	Parallel processor with functional pipeline providing programming engines by supporting multipl	20050823	718/107
US 6924991 B2	Energy transfer multiplexer	20050802	363/21.02
US 6906434 B1	Electric utility system with superconducting magnetic energy storage	20050614	307/64
US 6883152 B2	Voltage island chip implementation	20050419	716/5
US 6871150 B2	Expandable intelligent electronic device	20050322	702/62
US 6870410 B1	All digital power supply system and method that provides a substantially constant supply voltag	20050322	327/149
US 6859882 B2	System, method, and architecture for dynamic server power management and dynamic worklc	20050222	713/300
US 6844825 B2	Electric energy service apparatus with tamper detection	20050118	340/870.02
US 6820240 B2	Voltage island chip implementation	20041116	716/1
US 6819538 B2	Method and apparatus for controlling current demand in an integrated circuit	20041116	361/90
US 6819180 B2	Radio frequency power amplifier adaptive bias control circuit	20041116	330/285
US 6813571 B2	Apparatus and method for seamlessly upgrading the firmware of an intelligent electronic device	20041102	702/62
US 6792452 B1	Method for configuring a piece of equipment with the use of an associated machine resolvable	20040914	709/217
US 6792364 B2	Revenue meter with power quality features	20040914	702/61
US 6789001 B1	Digital multifunction relay	20040907	700/292
US 6784813 B2	Method, system, and apparatus for remote data calibration of a RFID tag population	20040831	341/53
US 6784568 B2	Device with multiple, concurrently-installed power modules and method for controlling same	20040831	307/66
US 6778534 B1	High-performance network processor	20040817	370/392
US 6766223 B1	Approach for managing power consumption of network devices	20040720	700/286
US 6748572 B2	Power supply network analyzing method, computer program for executing the method, storage	20040608	716/5
US 6745138 B2	Intelligent electronic device with assured data storage on powerdown	20040601	702/61
US 6735706 B2	Programmable power management system and method	20040511	713/300
US 6734797 B2	Identification tag utilizing charge pumps for voltage supply generation and data recovery	20040511	340/572.4
US 6704864 B1	Automatic configuration of equipment software	20040309	713/1
US 6694270 B2	Phasor transducer apparatus and system for protection, control, and management of electricity	20040217	702/57
US 6687627 B1	Electric charge storage apparatus and method for an intelligent electronic device	20040203	702/61
US 6686831 B2	Variable power control for process control instruments	20040203	340/310.01
US 6683494 B2	Digital signal processor enhanced pulse width modulation amplifier	20040127	330/10
US 6657424 B1	DC load detection in an electric utility meter	20031202	324/76.11
US 6650731 B1	Simulator for simulating an intelligent network	20031118	379/15.01
US 6636976 B1	Mechanism to control di/dt for a microprocessor	20031021	713/320
US 6631502 B2	Method of analyzing integrated circuit power distribution in chips containing voltage islands	20031007	716/4
US 6615147 B1	Revenue meter with power quality features	20030902	702/61
US 6594474 B1	Controlled-gain power amplifier device, in particular for radio-frequency circuits applied to cellul	20030715	455/127.2
US 6586971 B1	Adapting VLSI clocking to short term voltage transients	20030701	327/41
US 6586850 B1	Device with multiple, concurrently-installed power molecules and method for controlling same	20030701	307/85
US 6564328 B1	Microprocessor with digital power throttle	20030513	713/320
US 6552709 B1	Power-on display driving method and display driving circuit	20030422	345/99
US 6549867 B1	Power supply feed-forward compensation technique	20030415	702/107

Patent No.	Title	Date	Class
US 6549064 B2	Efficient charge pump apparatus	20030415	327/536
US 6536024 B1	Method for making integrated circuits having gated clock trees	20030318	716/6
US 6523154 B2	Method for supply voltage drop analysis during placement phase of chip design	20030218	716/6
US 6522243 B1	Geometric harmonic modulation signaling and detection	20030218	340/310.01
US 6507243 B2	Controller-based radio frequency amplifier module and method	20030114	330/129
US 6496783 B1	Electric power calculation system	20021217	702/65
US 6496342 B1	Distributed monitoring and protection system for a distributed power network	20021217	361/65
US 6493644 B1	A-base revenue meter with power quality features	20021210	702/61
US 6459175 B1	Universal power supply	20021001	307/149
US 6438678 B1	Apparatus and method for operating on data in a data communications system	20020820	712/34
US 6420935 B1	Controller-based radio frequency amplifier module and method	20020716	330/295
US 6405354 B1	Method and apparatus to optimize power wiring layout and generate wiring layout data for a se	20020611	716/8
US 6373340 B1	High-efficiency audio power amplifier	20020416	330/297
US 6356999 B1	Data processor with trie traversal instruction set extension	20020312	712/209
US 6311147 B1	Integrated circuit power net analysis	20011030	703/18
US 6281725 B1	Semiconductor integrated circuit having a clock recovery circuit	20010828	327/152
US 6278357 B1	Apparatus and method for implementing digital communications on a power line	20010821	340/310.01
US 6247162 B1	Method and apparatus for generating layout data for a semiconductor integrated circuit device	20010612	716/2
US 6243818 B1	Power subsystem for a communication network containing a power bus	20010605	713/300
US 6215261 B1	Application specific integrated circuit for controlling power devices for commutating a motor ba:	20010410	318/254
US 6212569 B1	Data processor with bit stuffing instruction set extension	20010403	709/236
US 6211661 B1	Tunable constant current source with temperature and power supply compensation	20010403	323/316
US 6209087 B1	Data processor with multiple compare extension instruction	20010327	712/300
US 6205487 B1	Data processor with bit unstuffing instruction set extension	20010320	709/236
US 6195755 B1	Nonvolatile power management apparatus for integrated circuit application	20010227	713/330
US 6192491 B1	Data processor with CRC instruction set extension	20010220	714/52
US 6175915 B1	Data processor with trie traversal instruction set extension	20010116	712/227
US 6154686 A	Distributed architecture	20001128	700/276
US 6125448 A	Power subsystem for a communication network containing a power bus	20000926	713/300
US 6088288 A	Memory device and method of reducing ground bounce in a memory device	20000711	365/230.06
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US 6047245 A	Resistive strain gauge control circuit	20000404	702/104
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Patent	Title	Date	Class
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US 5796631 A	Method and apparatus for monitoring and characterizing power quality, faults and other phenon	19980818	713/340
US 5793194 A	Bias circuit having process variation compensation and power supply variation compensation	19980811	323/312
US 5786709 A	Integrated circuit output driver incorporating power distribution noise suppression circuitry	19980728	326/27
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US 5148167 A	Sigma-delta oversampled analog-to-digital converter network with chopper stabilization	19920915	341/143
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10/040582   Edward Grochoswski et al.

EAST SEARCH

9/2/05

L#	Hits	Search String	Databases
S68	3920	((integrated or digital) near2 circuit$1) and (power near2 network)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S69	0	S68 and ("sucessive clocks".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S70	0	S68 and ("sucessive cycles".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S71	0	S68 and ("sucessive intervals".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S72	21	S68 and ("clock cycles".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S73	13	S68 and ("current consumption".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S74	10	S68 and ("current amplitudes".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S75	71	S68 and ("current values".CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S76	108	S72 or S73 or S74 or S75	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S77	29	S76 and (clocks.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S78	34	S76 and (cycles.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S79	21	S76 and (intervals.CLM.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
S80	47	S77 or S78 or S79	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB

10/040582   Edward Grochoswski et al.

EAST SEARCH

9/2/05

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Patent No.	Title	Date	Class
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US 6823293 B2	Hierarchical power supply noise monitoring device and system for very large scale integrated c	20041123	702/191
US 6754748 B2	Method and apparatus for distributing multi-source/multi-sink control signals among nodes on a	20040622	710/100
US 6711719 B2	Method and apparatus for reducing power consumption in VLSI circuit designs	20040323	716/2
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